	Application No.	Applicant(s)
Notice of Allowability	10/797,160	SUNG, CHIH-FENG
	Examiner	Art Unit
	Prabodh M. Dharia	2629
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>03-31-2007</u> .		
2. The allowed claim(s) is/are <u>1-16</u> .		
<ul> <li>3.  Acknowledgment is made of a claim for foreign priority of a)  All b)  Some* c)  None of the: <ol> <li>Certified copies of the priority documents have</li> <li>Certified copies of the priority documents have</li> <li>Copies of the certified copies of the priority documents have</li> <li>Copies of the certified copies of the priority documents have</li> <li>Copies of the certified copies of the priority documents have</li> <li>Tertified copies not received:</li> </ol> </li> <li>Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONI</li> </ul>	re been received. The been received in Application Not becoments have been received in the communication to file a re	o his national stage application from the
<ul> <li>THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.</li> <li>4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient:</li> <li>5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.</li> </ul>		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1) hereto or 2) to Paper No./Mail Date  (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date  Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).  6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s)	5.  Notice of Inform	al Patent Application
<ol> <li>Notice of References Cited (PTO-892)</li> <li>Dotice of Draftperson's Patent Drawing Review (PTO-948)</li> </ol>	6. 🗌 Interview Summ	ary (PTO-413),
3. Information Disclosure Statements (PTO/SB/08),	Paper No./Mail 7.	
Paper No./Mail Date  4.  Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's Stat 9. □ Other	ement of Reasons for Allowance
·		SUMATI LEFKOWITZ

U.S. Patent and Trademark Office PTOL-37 (Rev. 08-06) SUPERVISORY PATENT EXAMINER

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1. Status: Please all the replies and correspondence should be addressed to Examiner's new art unit 2629. Receipt is acknowledged of papers submitted on 03-31-2007 under request for reconsideration, which have been placed of record in the file. Claims 1-16 are pending in this action.

## Response to Amendment

1. Claims 3,4,7,8,11,12,15 and 16 were allowed. Applicant's argument, see Remark, filed 03-31-2007, with respect to Claims 1,5,9,and 13 regarding "the present application. In Fig. 4, when the equivalent channel width/length (W/L) ratio equals 10, the a-Si TFT outputs a 6.13 μA current by inputting only 7 volt input voltage. As to current leakage, performance of the a-Si TFT is superior to an LTPS TFT. Thus, the a-Si TFT can be applied in an OLED display by raising the W/L above 10. That is, when equivalent channel width/length (W/L) ratio of the transistor exceeds 10, performance of the a-Si TFT is superior to an LTPS TFT" have been fully considered, extensively searched and are persuasive as they do overcome prior art rejection; which puts application number 10797160 in condition for allowance. The prior art rejection mailed on 01-09-2007 is withdrawn.

## Allowable Subject Matter

- 2. Claims 1-16
- 3. The following is an examiner's statement of reasons for allowance:

Applicant's arguments filed on 02-01-2007 are convincing. As argued by applicant in remarks under claim rejection page 11, paragraph, 3 and 4, the prior art of Libsch et al. (US

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2003/0107565 A1) in view of Nathan et al. (US 2006/0027807A1) fails to recite or disclose the uniquely distinct features represented by underlined bold claim below;

an active matrix organic light-emitting diode (OLED) display, having a cathode coupled to a drain terminal of the second transistor and an anode coupled to a first voltage exceeding the second voltage; wherein the second transistor is an amorphous silicon thin film transistor (a-Si TFT); and an equivalent channel width/length (W/L) ratio of the second transistor exceeds 10.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance.

## Conclusion

- 4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Prabodh M Dharia whose telephone number is 571-272-7668. The examiner can normally be reached on M-F 8AM to 5PM.
- 5. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.
- 6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

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system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

Prabodh Dharia

Partial Signatory Authority

AU2629

May 03, 2007

SUMATI LEFKOWITZ SUPERVISORY PATENT EXAMINER